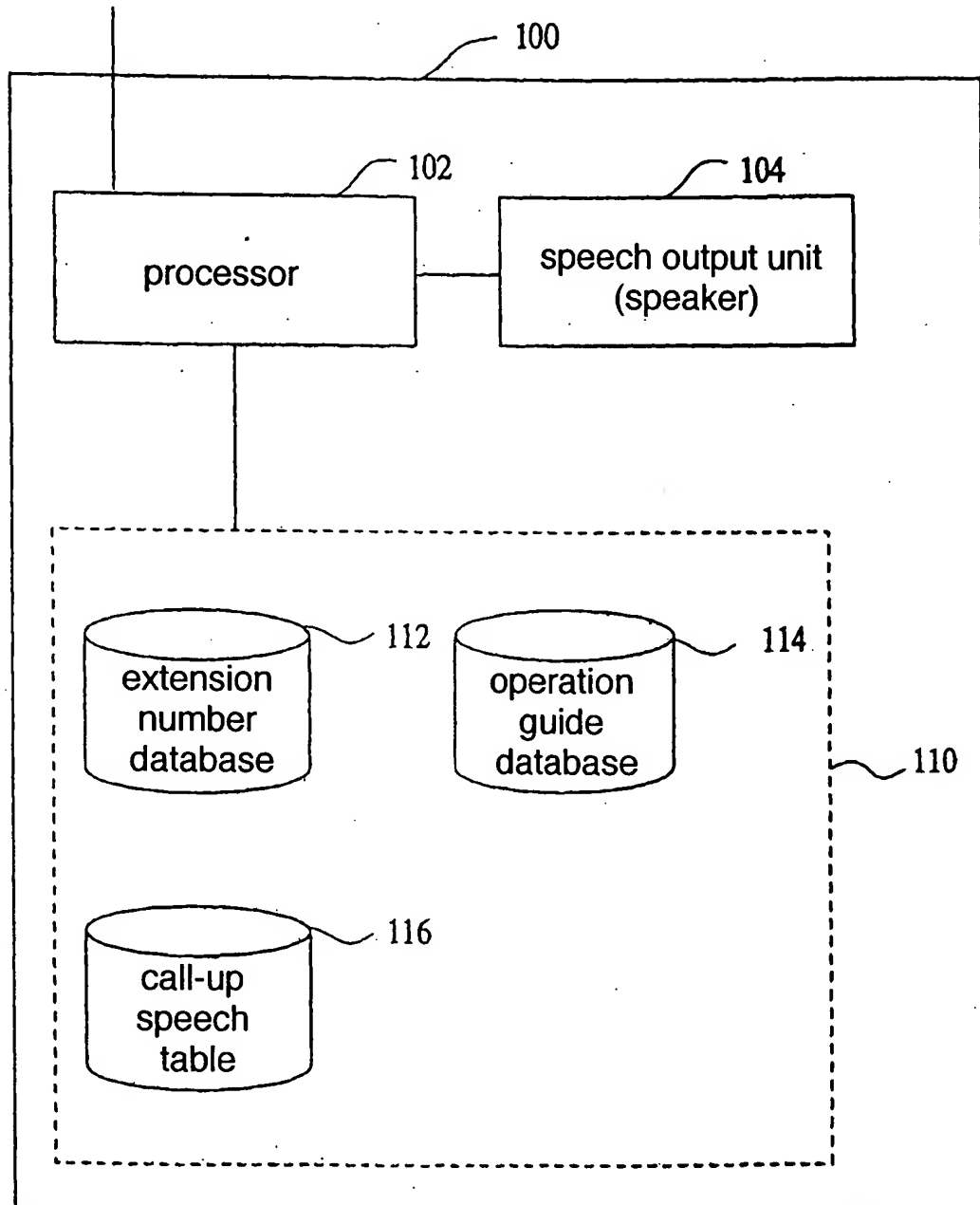


FIG. 1

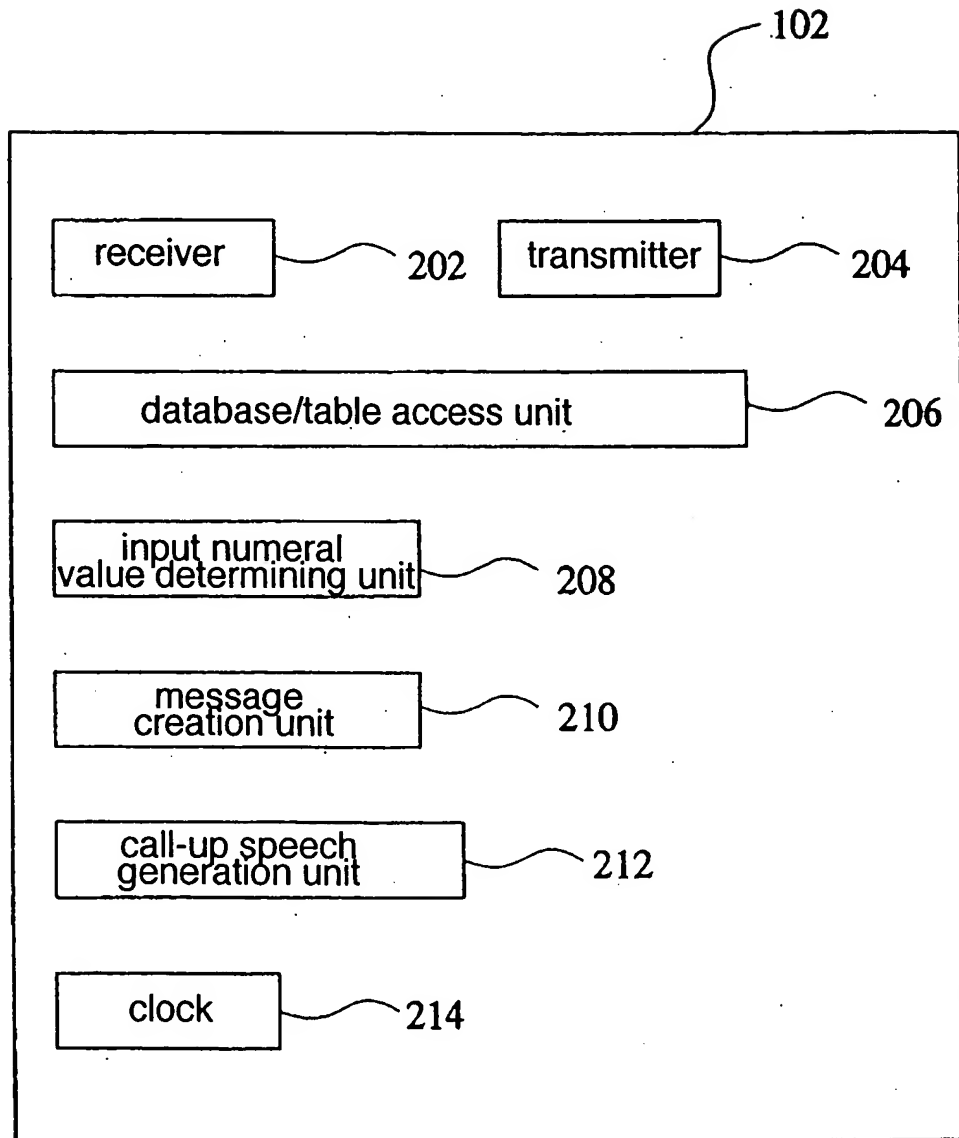
to communication circuit



100: communication terminal equipment 110: memory

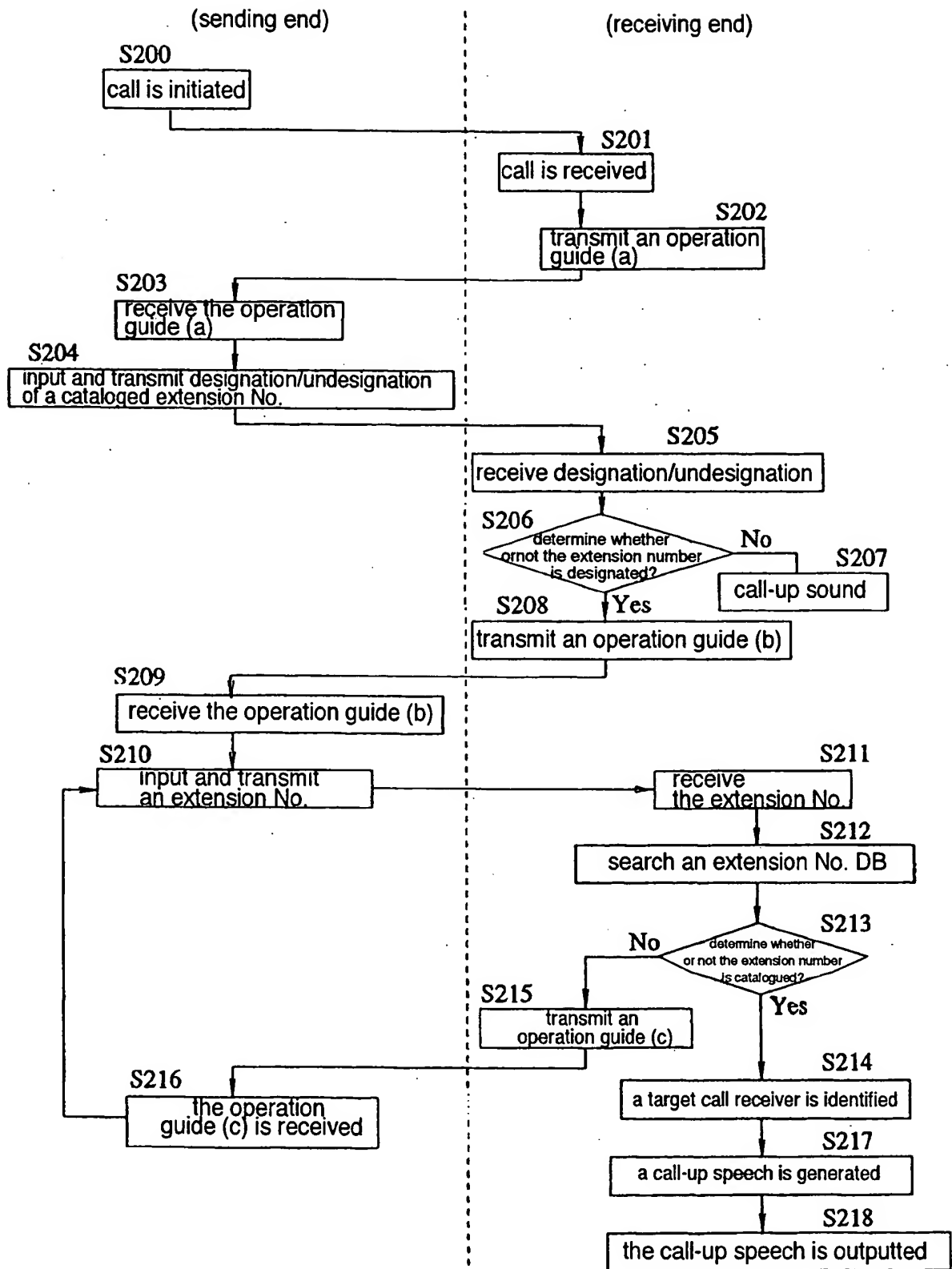
BLOCK DIAGRAM OF A COMMUNICATIONS TERMINAL
EQUIPMENT ACCORDING TO FIRST EMBODIMENT OF THE INVENTION

FIG. 2



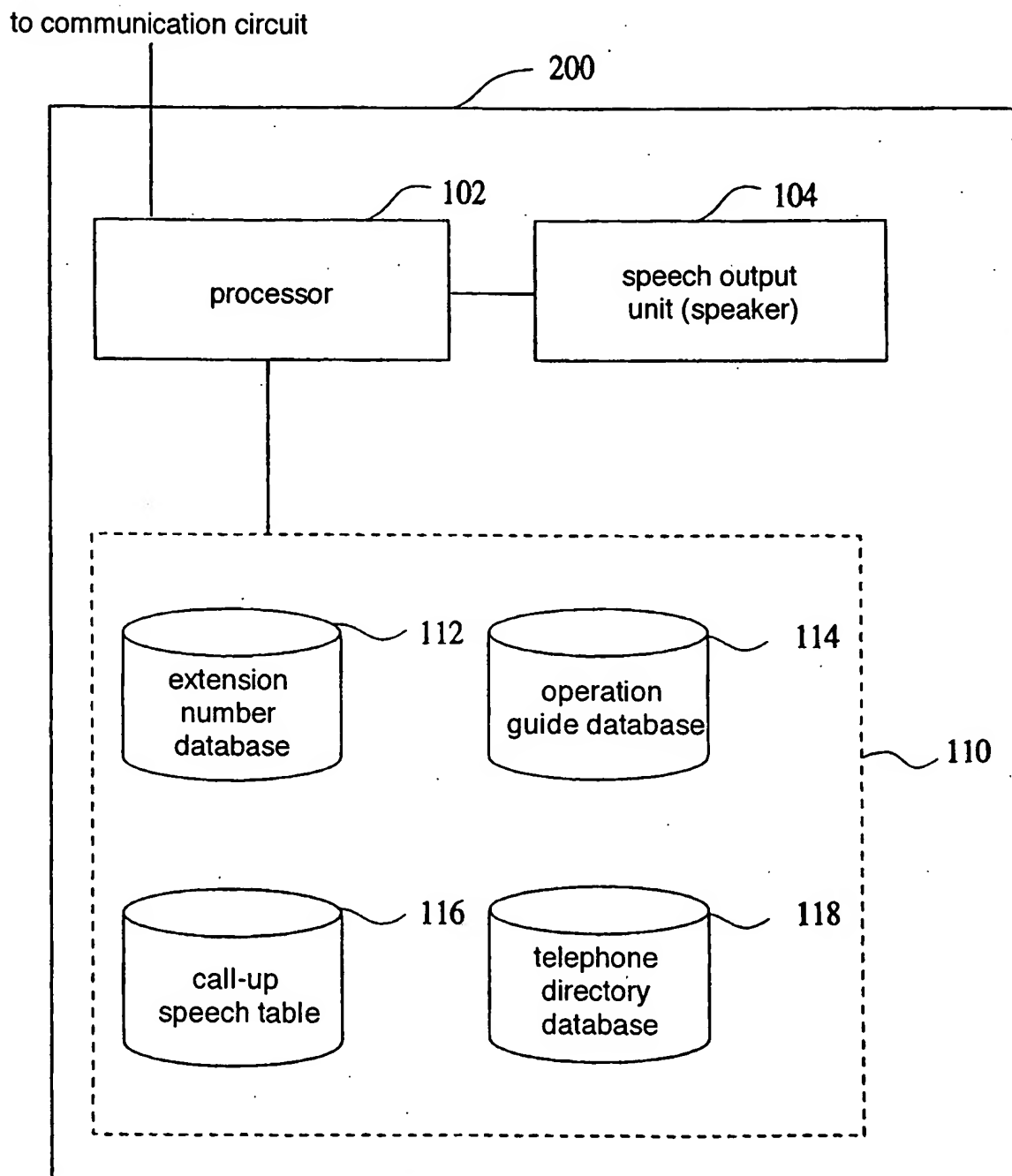
RESPECTIVE COMPONENTS INSIDE THE PROCESSOR

FIG. 3



FLOW CHART SHOWING PROCESSING
ACCORDING TO FIRST EMBODIMENT OF THE INVENTION

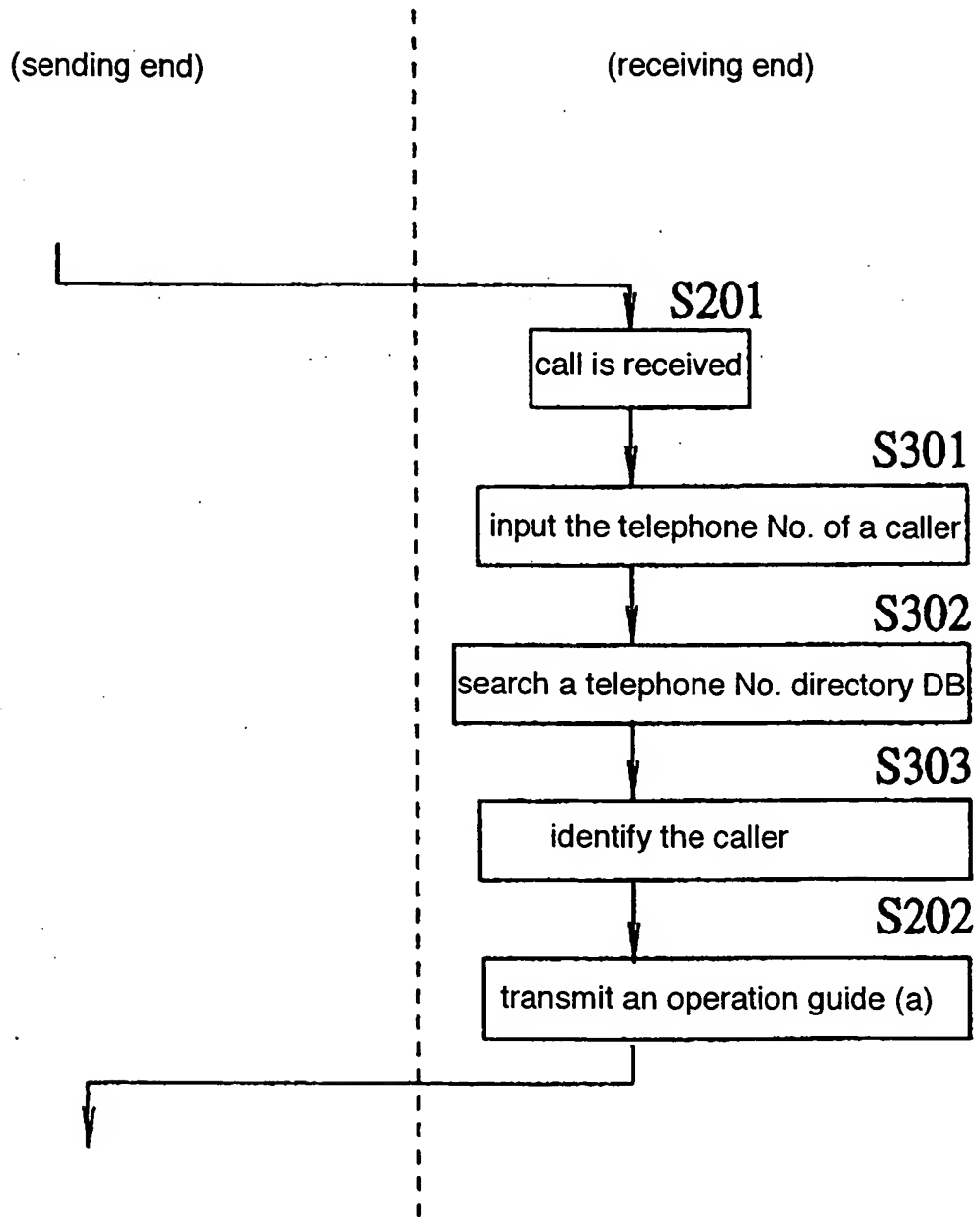
FIG. 4



200: communication terminal equipment

BLOCK DIAGRAM OF A COMMUNICATIONS TERMINAL
EQUIPMENT ACCORDING TO SECOND EMBODIMENT OF THE INVENTION

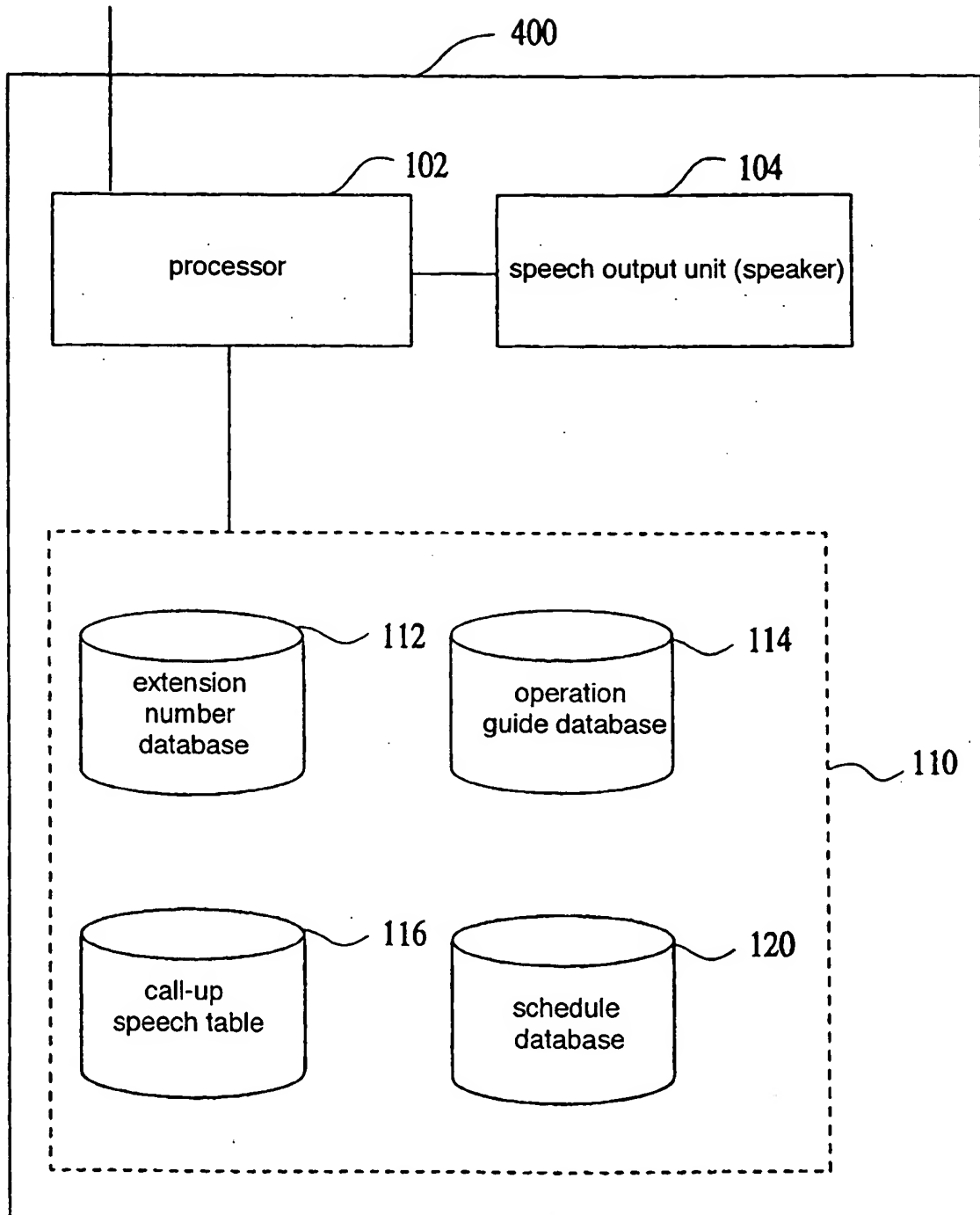
FIG. 5



FLOW CHART SHOWING PARTS OF PROCESSING
ACCORDING TO SECOND EMBODIMENT OF THE INVENTION

FIG. 6

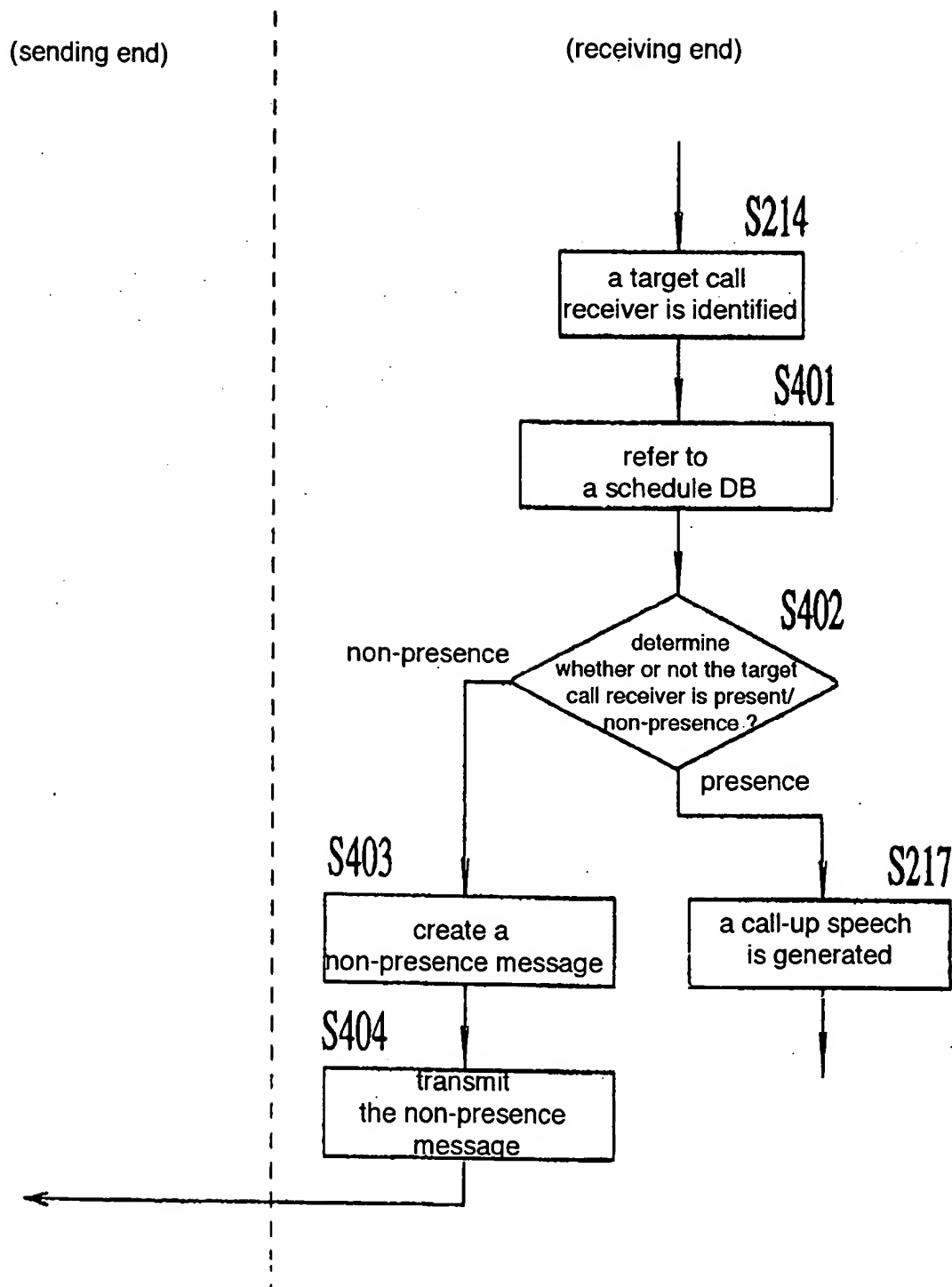
to communication circuit



400: communication terminal equipment

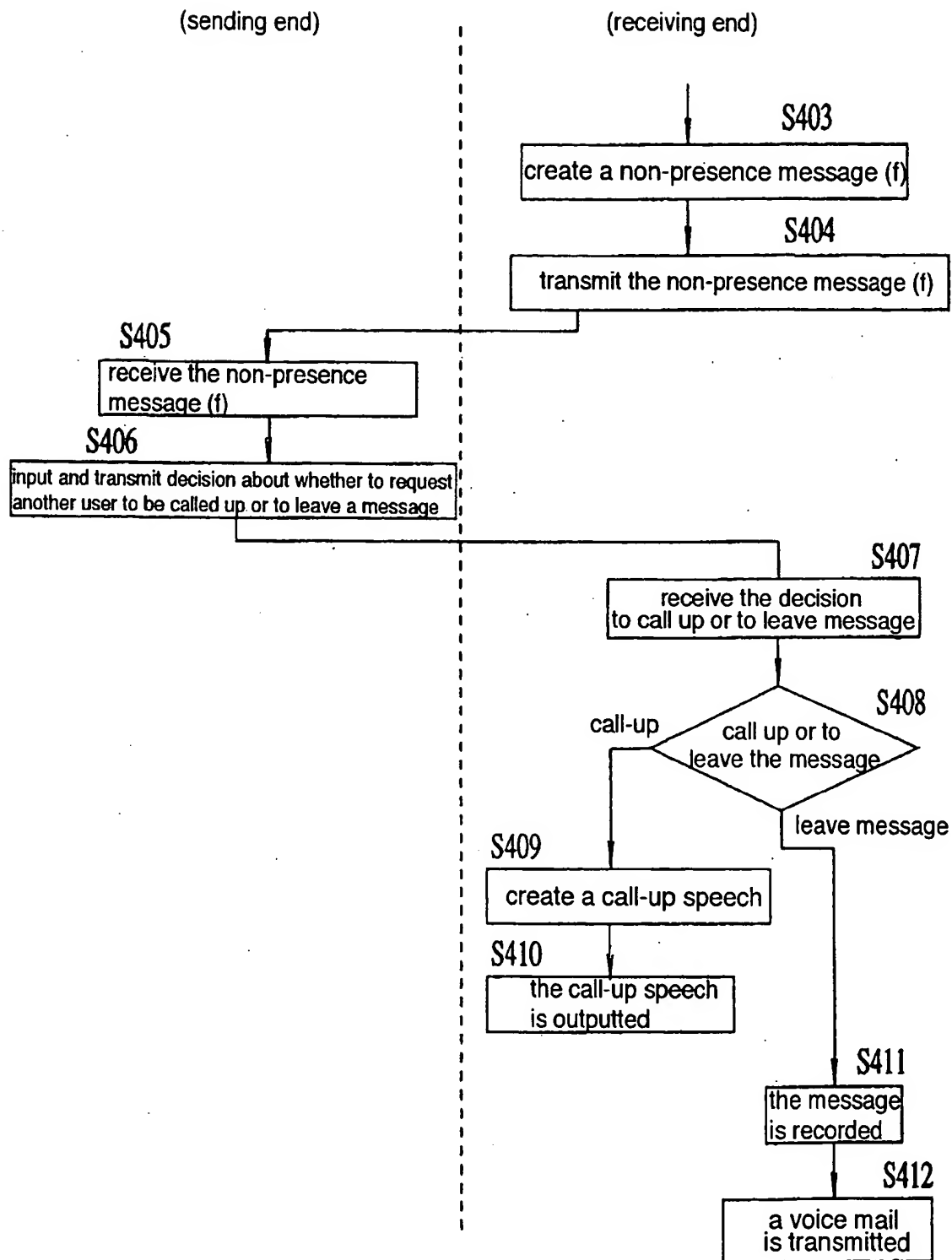
BLOCK DIAGRAM OF A COMMUNICATIONS TERMINAL
EQUIPMENT ACCORDING TO FOURTH EMBODIMENT OF THE INVENTION

FIG. 7



FLOW CHART SHOWING PARTS OF PROCESSING
ACCORDING TO FOURTH EMBODIMENT OF THE INVENTION

FIG. 8



FLOW CHART SHOWING PARTS OF PROCESSING
ACCORDING TO A VARIATION TO FOURTH EMBODIMENT OF THE INVENTION